

AMENDMENTS TO THE CLAIMS:

This listing of claims replaces all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (Currently Amended) A switching topology for automatic test equipment, comprising:
a plurality of switching circuits each having first through fourth nodes, wherein the first and second nodes are connectable to the third and fourth nodes, respectively, to form a THROUGH connection, or to the fourth and third nodes, respectively, to form a CROSSED connection,

wherein each of the first through fourth nodes of any of the plurality of switching circuits is connected to at most one of the first through fourth nodes of all the others of the plurality of switching circuits and each of the plurality of switching circuits comprises first and second switching elements each having a common point and first and second connection points to which the common point is selectively connectable.

Claim 2 (Cancelled)

3. (Currently Amended) A switching topology as recited in claim 1 2, wherein, for each of the plurality of switching circuits,

the first and second nodes are the common points of the first and second switching elements, respectively,

one of the first and second connection points of the first switching element is connected to one of the first and second connection points of the second switching element to form the third node of the switching circuit,

the other of the first and second connection points of the first switching element is connected to the other of the first and second connection points of the second switching element to form the fourth node of the switching circuit.

4. (Original) A switching topology as recited in claim 3, wherein the first and second switching elements are form-C relays.

5. (Original) A switching topology as recited in claim 4, wherein the form-C relays for the first and second switching elements are located together in a dual form-C package.

6. (Original) A switching topology as recited in claim 5, wherein all connections between the first and second switching elements are formed within the dual form-C package.

7. (Original) A switching topology as recited in claim 5, wherein the first and second switching elements are controlled together in response to a single control signal.

8. (Original) A switching topology as recited in claim 1, wherein the switching circuits are implemented with micro-machined switches.

9. (Original) A switching topology as recited in claim 1, wherein the switching circuits are implemented with solid-state switches.

Claims 10 to 20 (Cancelled)